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This book constitutes the thoroughly refereed proceedings of the 14th International Conference on Collaborative Computing: Networking, Applications, and Worksharing, CollaborateCom 2018, held in Shanghai, China, in December 2018. The 43 full and 19 short papers presented were carefully reviewed and selected from 106 submissions. The papers reflect the conference sessions as follows: vehicular networks; social networks, information processing, data detection and retrieval & mobility, parallel computing, knowledge graph, cloud and optimization & software testing and formal verification; collaborative computing, social networks, vehicular networks, networks and sensors, information processing and collaborative computing, mobility and software testing and formal verification, web services and image information processing, web services and remote sensing.

Maximum PC is the magazine that every computer fanatic, PC gamer or content creator must read. Each and every issue is packed with punishing product reviews, insightful and innovative how-to stories and the illuminating technical articles that enthusiasts crave.

This book provides an overview of recent advances in memory interface design at both the architecture and circuit levels. Coverage includes signal integrity and testing, TSV interface, high-speed serial interface including equalization, ODT, pre-emphasis, wide I/O interface including crosstalk, skew cancellation, and clock generation and distribution. Trends for further bandwidth enhancement are also covered.

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This book brings together papers from the 2018 International Conference on Communications, Signal Processing, and Systems, which was held in Dalian, China on July 14 – 16, 2018. Presenting the latest developments and discussing the interactions and links between these multidisciplinary fields, the book spans topics ranging from communications, signal processing and systems. It is aimed at undergraduate and graduate electrical engineering, computer science and mathematics students, researchers and engineers from academia and industry as well as government employees.

Provides coverage of topics found on the test, two practice exams with answers and explanations, and access to web-based practice questions.

Digital Design and Computer Architecture Second Edition David Money Harris and Sarah L. Harris "Harris and Harris have taken the popular pedagogy from Computer Organization and Design down to the next level of refinement, showing in detail how to build a MIPS microprocessor in both Verilog and VHDL. Given the exciting opportunity that students have to run large digital designs on modern FPGAs, the approach the authors take in this book is both informative and enlightening." -David A. Patterson, University of California at Berkeley, Co-author of Computer Organization and Design Digital Design and Computer Architecture takes a unique and modern approach to digital design. Beginning with digital logic gates and progressing to the design of combinational and sequential circuits, Harris and Harris use these fundamental building blocks as the basis for what follows: the design of an actual MIPS processor. SystemVerilog and VHDL are integrated throughout the text in examples illustrating the methods and techniques for CAD-based circuit design. By the end of this book, readers will be able to build their own microprocessor and will have a top-to-bottom understanding of how it works. Harris and Harris have combined

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an engaging and humorous writing style with an updated and hands-on approach to digital design. This second edition has been updated with new content on I/O systems in the context of general purpose processors found in a PC as well as microcontrollers found almost everywhere. The new edition provides practical examples of how to interface with peripherals using RS232, SPI, motor control, interrupts, wireless, and analog-to-digital conversion. High-level descriptions of I/O interfaces found in PCs include USB, SDRAM, WiFi, PCI Express, and others. In addition to expanded and updated material throughout, SystemVerilog is now featured in the programming and code examples (replacing Verilog), alongside VHDL. This new edition also provides additional exercises and a new appendix on C programming to strengthen the connection between programming and processor architecture. **SECOND Edition Features** Covers the fundamentals of digital logic design and reinforces logic concepts through the design of a MIPS microprocessor. Features side-by-side examples of the two most prominent Hardware Description Languages (HDLs)-SystemVerilog and VHDL-which illustrate and compare the ways each can be used in the design of digital systems. Includes examples throughout the text that enhance the reader's understanding and retention of key concepts and techniques. Companion Web site includes links to CAD tools for FPGA design from Altera and Mentor Graphics, lecture slides, laboratory projects, and solutions to exercises. David Money Harris Professor of Engineering, Harvey Mudd College Sarah L. Harris Associate Professor of Engineering, Harvey Mudd College Updated based on instructor feedback with more exercises and new examples of parallel and advanced architectures, practical I/O applications, embedded systems, and heterogeneous computing Presents digital system design examples in both VHDL and SystemVerilog (updated for the second edition from Verilog), shown side-by-side to compare and contrast their strengths Includes a new chapter on C programming to provide necessary prerequisites and strengthen the

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connection between programming and processor architecture
Companion Web site includes links to Xilinx CAD tools for FPGA design, lecture slides, laboratory projects, and solutions to exercises. Instructors can also register at textbooks.elsevier.com for access to: Solutions to all exercises (PDF) Lab materials with solutions HDL for textbook examples and ex

Discover one of the most comprehensive introductions to information systems hardware and software in business today with Burd ' s SYSTEMS ARCHITECTURE, 7E. This new edition remains an indispensable tool for current and future IS (Information Systems) professionals with a managerial, broad systems perspective that provides a holistic approach to systems architecture. This edition has been thoroughly updated to ensure all concepts, examples and applications reflects the latest in today ' s new and emerging technologies. Important Notice: Media content referenced within the product description or the product text may not be available in the ebook version.

This book constitutes the thoroughly refereed post-conference proceedings of the 9th International Conference on Security for Information Technology and Communications, SECITC 2016, held in Bucharest, Romania, in June 2016. The 16 revised full papers were carefully reviewed and selected from 35 submissions. In addition with 4 invited talks the papers cover topics such as Cryptographic Algorithms and Protocols, and Security Technologies for ITC.

"DRAM Circuit Design" teaches readers the introductory level design of DRAM memory chips. It focuses on giving readers a reference that can be used to educate students or practicing design engineers in DRAM circuit design.

Annotation An arsenal of study aids for anyone preparing to take

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the CompTIA A+ certification exams, this book is the industry standard in terms of measuring a technician's hardware and software knowledge. The topics covered include managing hardware, IT troubleshooting and maintenance, networking, and more.

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